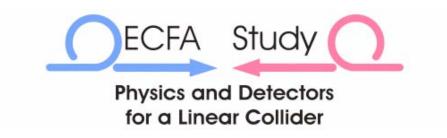
A Review of Tracking Sessions

Madhu S. Dixit TRIUMF & Carleton University

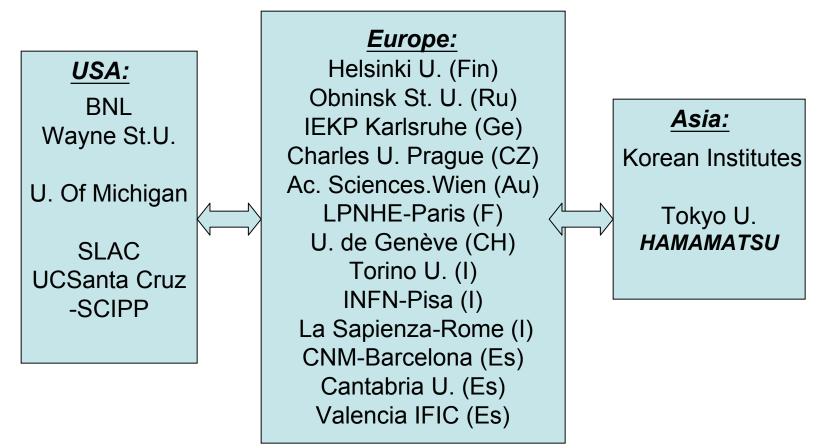


Durham ECFA Workshop 1 - 4 September 2004

8 minutes time for this summary (allow 2 minutes for questions) 3 tracking sessions lasting ~4.5 hours (13 talks 20 minutes each)

<u>TPC R&D (GEMs, Micromegas, wires)</u> <u>Magnetic field & beam tests, 2 track studies, new readout</u> <u>concepts, ion feedback studies and field cage design</u> <u>10/13 talks (76% => allocate 6.2 min for the TPC summary)</u>	2 talks on <u>Silicon</u> (15%=>1.2 min)	<u>1 Forward</u> <u>tracker talk</u> (5%=>0.6min)
 Adrian Vogel - Charge broadening & ion feedback in triple GEM TPC Ties Behnke - TPC activities at DESY Sabine Blatt - Design & construction of GEM-TPC Paul Colas - Micromegas TPC tests Jan Timmermans - Status of Si-readout studies at Nikhef Akira Sugiyama - MPI TPC beam tests at KEK Dean Karlen - Victoria TPC Tests in DESY 5 T magnet Madhu Dixit - Charge dispersion resolution in GEM-TPC Alexander Kaukher - A GEM TPC with TDC readout Dean Karlen - A proposal to build a CO₂ TPC for T2K 	 Status report on SiLC Frederic Kapusta Electronics & DAQ for SiLC Jean-Francois Genat 	Status of forward straw tubes simulation - Klaus Moenig

Status report on the SiLC R&D activities - Kapusta



A international collaborative generic R&D effort studying BOTH a all-Silicon-tracking system (SiD) and a TPC + Silicon tracking (GLC/TESLA/LD)
Activities: Hardware, simulation integration issues.

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R&D sensors - one example

Main R&D objectives:

- Long microstrips (long ladders)
- ➢ Si Drift
- ➢ Keeping an eye on new Si-tech (pixelisation, etc...)

Main requests: TRANSPARENCY, PRECISION & BETTER YIELD

increased wafers from 6" to possibly 12" thinner and smaller pitch

Expressed interest: Hamamatsu (Now officially part of SiLC)

ST Microelectronics/Catania (tbc) CNM-Barcelona as R&D center Others ? New comers are welcome For Si-drift several European teams in STAR, ALICE have good connections with various firms (Canberra ...)

Lot of expertise from LEP, CDF, and now LHC (ATLAS, CMS and ALICE) Vienna responsible for coordinating the R&D on sensors & contacts with industry (also presently in charge in CMS).

Main actions: collaboration with industry based on established connections & test quality procedures for LHC to monitor the R&D & production on sensors. Durham 4/9/2004 Silicon tracker data acquisition and electronics development for the Linear Collider –Jean-Francois Genat

New VLSI technologies:

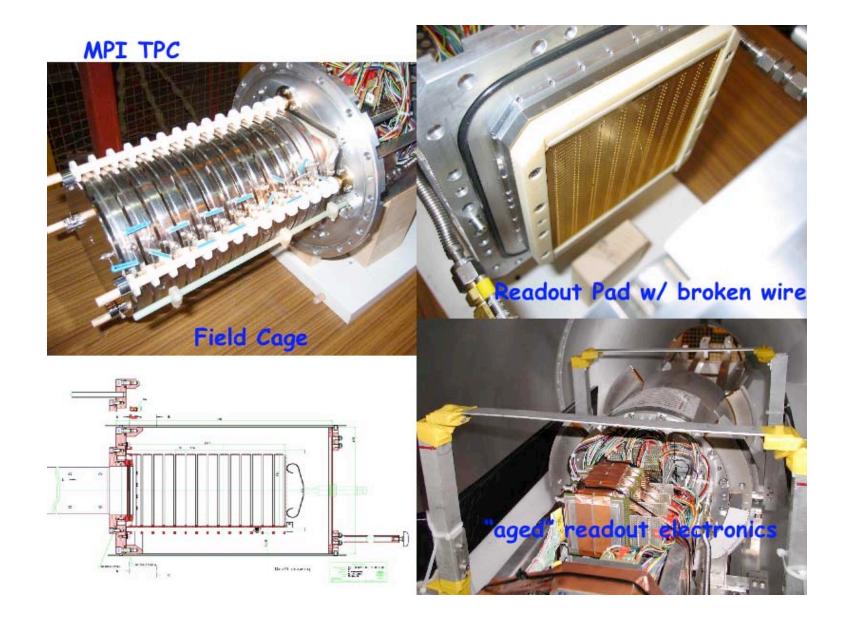
- Silicon Deep Sub Micron CMOS
- Silicon-Germanium alternative (incorporate DSM CMOS)

Progress in implement a highly integrated front end for SiLC that does not degrade the detector resolution An affordable power and material budget Implement system integration; e.g. data compaction, cluster centroid and fast tracking algorithms

MPI/TPC prototype beamtest at KEK and future plan

by Akira Sugiyama (Saga Univ.) for Europa/Asia TPC collaboration (Not an official name yet!)

Collaboration Goal of this this collab. Facilities at KEK Results Future plan



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Summary for TPC w/ MWPC

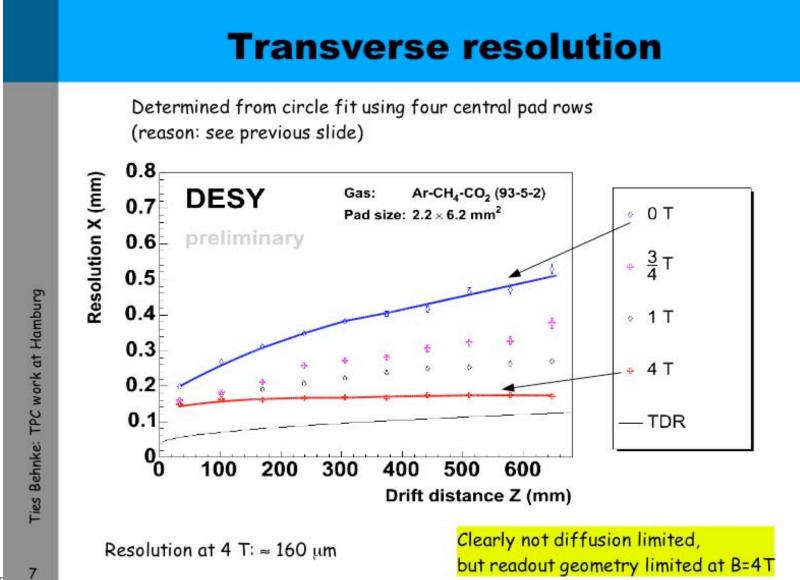
We start MPI/TPC operation at KEK using beam and CR Analysis is on going. some basic parameters are observed, but very preliminary σ_x ~ 200(@0 drift) - 300um(@20cm drift w/1T), σ_z ~ 400-600um small diff. w/ w/o B

We need to study more detail w/ calib., corr. comparison to Simulation is necessary.

Status/Plan MPGD readout for TPC

GEM readout is tested using beam Preparation to install it into TPC is on going.

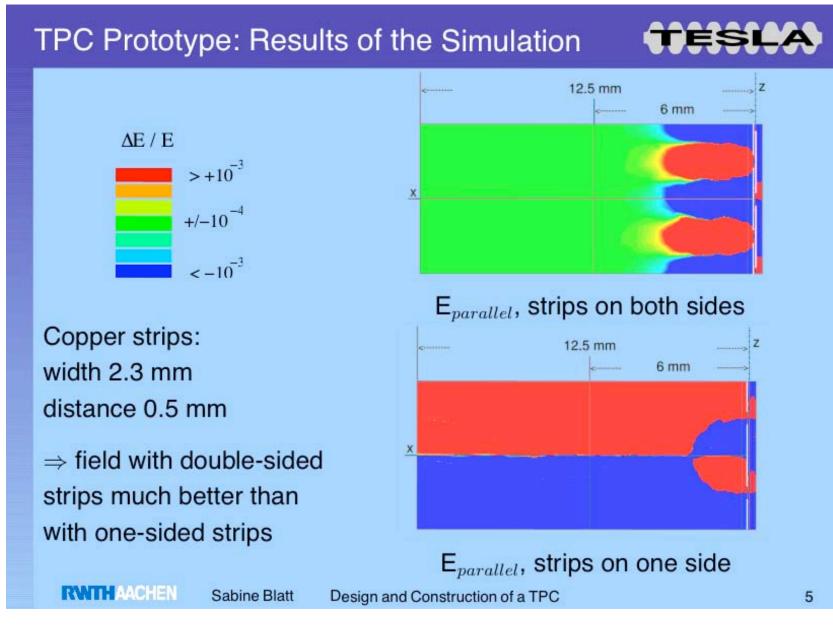
MPGD R&D for LC/TPC will be started







- Development of a TPC prototype
 - Optimisation of the fieldcage
 - Construction and first measurements
 - Readout electronics
- TPC simulation



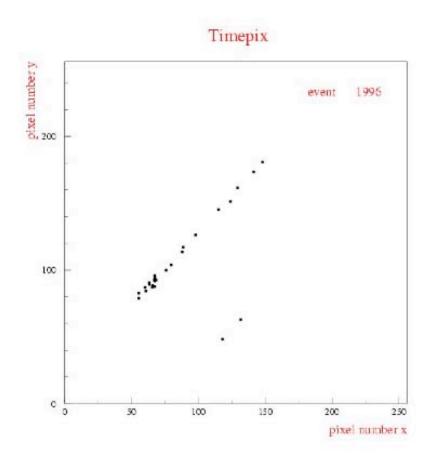
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Status 'Si readout' TPC at NIKHEF - Jan Timmermans

Goals

- Gas multiplication GEM or Micromegas foil(s)
- Charge collection with granularity matching primary ionisation cluster spread
- Needs sufficiently low diffusion gas
- dE/dx using cluster counting?
 (→ M. Hauschild)
- Proof of principle based on existing Medipix2 readout chip

Example of a track reconstructed

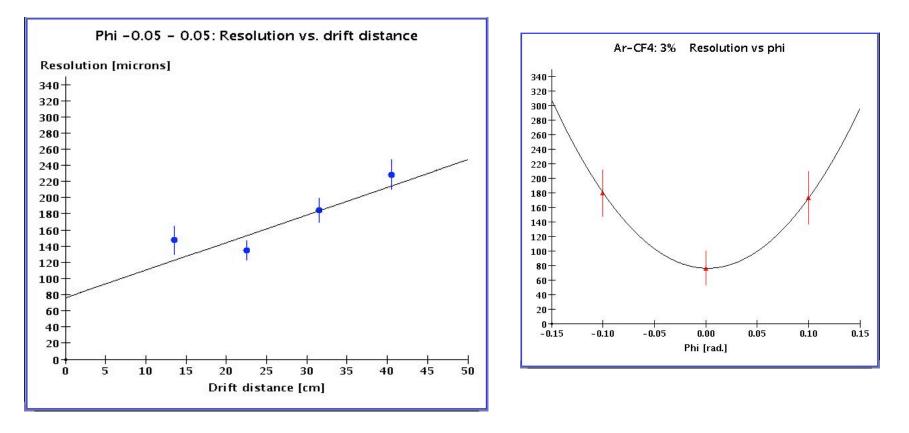


This track: •#hits = 24 •#clusters = 11 •length (3d) = 16.8 mm •1.4 e⁻/mm; 0.65 cl./mm On average:

•1.8 e⁻/mm; 0.5 cl./mm

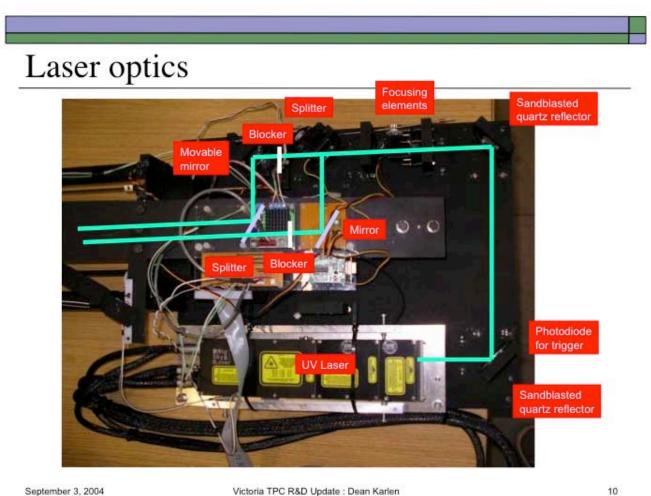
Micromegas TPC Resolution in a Magnetic Field - Colas

Transverse position resolution in Ar/CF4 3%



80 micron resolution for vertical tracks using 1X10 mm² pads.

Victoria TPC R&D Update - 2 track TPC resolution for laser tracks in a 4 T field - Karlen

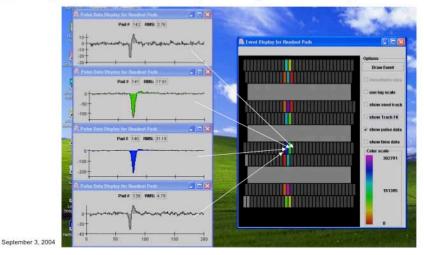




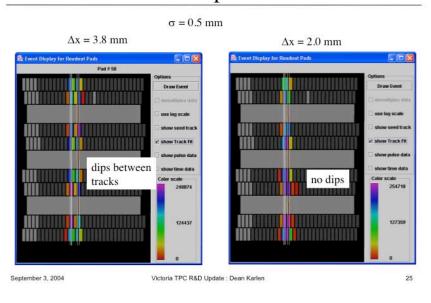
Victoria TPC R&D Update : Dean Karlen

DESY run at 4 T

□ Single laser track seen by 2 mm pads and P5 gas



Track fits: 2mm wide pads



□ Two track resolution is quite good: 2-4 mm for 2 mm pads

New concept of charge dispersion for improved resolution in a GEM-TPC

•Modified GEM anode structure with a high resistivity film bonded to the readout plane with an insulating layer of glue.

•2-dim RC network defined by material properties & geometry.

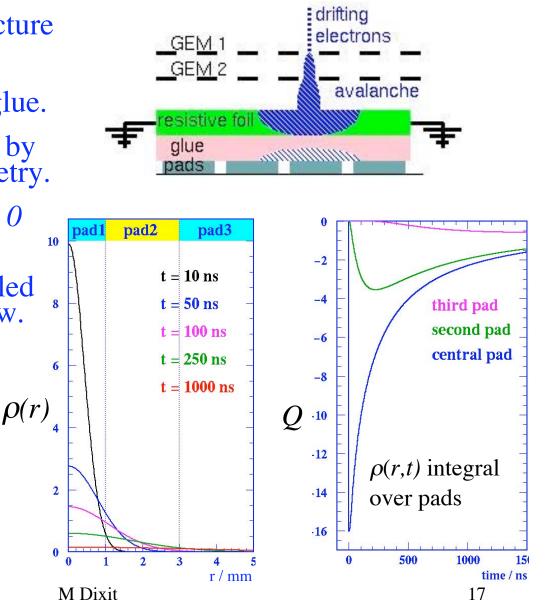
•Point charge at r = 0 & t = 0 disperses with time.

•Measure capacitively coupled charge signals on pads below.

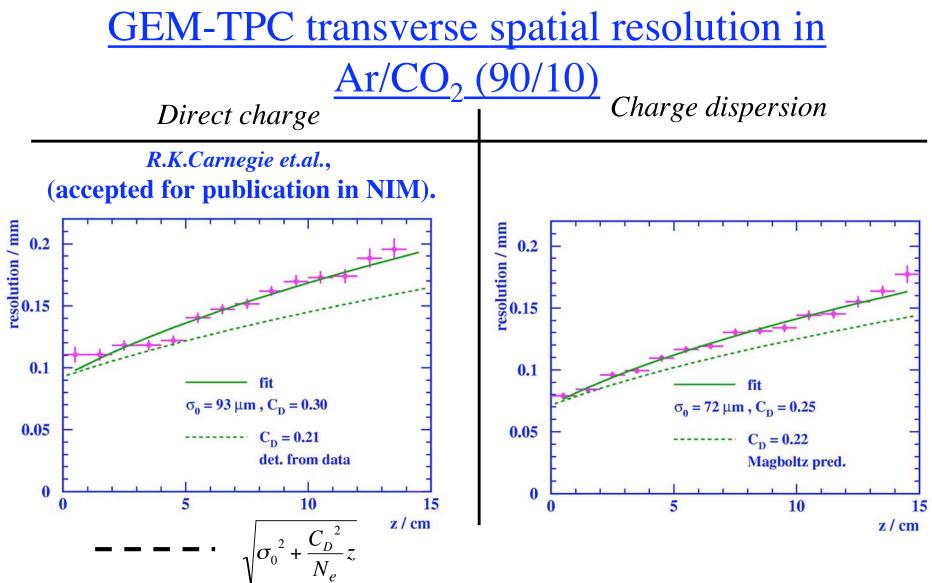
Telegraph equation for the charge density function:

$$\frac{\partial \rho}{\partial t} = \frac{1}{RC} \left[\frac{\partial^2 \rho}{\partial r^2} + \frac{1}{r} \frac{\partial \rho}{\partial r} \right]$$
$$\Rightarrow \rho(r,t) = \frac{RC}{2t} e^{\frac{-r^2 RC}{4t}}$$

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- Dixit



With charge dispersion resolution close to diffusion limit possible for the TPC

~ 70 μm resolution may be within reach for the ILC TPC for all drift distances Durham 4/9/2004 M Dixit 18

Summary - gaseous tracking

Progress in many areas; both in hardware and in simulation Many labs working on prototype - this is needed, however, to cover various aspects of detector development. Prototypes test results encouraging. Micropattern detectors look promising for the ILC TPC readout; resolution somewhat worse than design requirements has been reached with possibility for further improvement. R&D at a stage where the choice of TPC readout technology

appears feasible in ~ 2 years. However, more effort is needed in the area of developing the readout electronics. Apologies to those whose work is not mentioned in this summary.